

Computer System Architecture

COMP201TH

Lecture-5

Don't Care Conditions

- **Duality Theorem of Boolean Algebra:**

- This theorem states that the dual of the Boolean function is obtained by interchanging the logical AND operator with logical OR operator and zeros with ones. For every Boolean function, there will be a corresponding Dual function.

Given Expression	Dual	Given Expression	Dual
$\bar{0} = 1$	$\bar{1} = 0$	$A \cdot (A+B) = A$	$A + A \cdot B = A$
$0 \cdot 1 = 0$	$1 + 0 = 1$	$\overline{AB} = \bar{A} + \bar{B}$	$\overline{A+B} = \bar{A} \cdot \bar{B}$
$A \cdot 0 = 0$	$A + 1 = 1$	$(A+C) \cdot (\bar{A} + B) = AB + AC$	$AC + \bar{A}B = (A+B) \cdot (A+\bar{C})$
$A \cdot B = B \cdot A$	$A + B = B + A$	$A+B = AB + \bar{A}B + A\bar{B}$	$AB = (A+B) \cdot (\bar{A}+B) \cdot (A+\bar{B})$
$A \cdot \bar{A} = 0$	$A + \bar{A} = 1$	$\overline{AB + \bar{A} + AB} = 0$	$\overline{((A+B)) \cdot \bar{A} \cdot (A+B)} = 1$
$A \cdot (B \cdot C) = (A \cdot B) \cdot C$	$A + (B + C) = (A + B) + C$		

- **Complement of a function (Using De-Morgan's Law):**

- De-morgan's law :
 - $(xy)^\sim = x^\sim + y^\sim$
 - $(x+y)^\sim = x^\sim \cdot y^\sim$
- E.g. find the complement of function $f = AB + CD + BD^\sim$

$$\begin{aligned}
 f^\sim &= (AB+CD+BD^\sim)^\sim \\
 &= (AB)^\sim \cdot (CD)^\sim \cdot (BD^\sim)^\sim \\
 &= (A^\sim + B^\sim) \cdot (C^\sim + D^\sim) \cdot (B^\sim + (D^\sim)^\sim) \\
 &= (A^\sim + B^\sim)(C^\sim + D^\sim)(B^\sim + D)
 \end{aligned}$$

- **Product of Sums Simplification:**

- In previous lecture, the Boolean expressions we derived from the maps were expressed in sum-of-products form.
- The 1's in the map represent the minterms that produce 1 for the function. The squares not marked by 1 represent the minterms that produce 0 for the function. If we mark the empty squares with 0's and combine them into groups of adjacent squares, we obtain the complement of the function f. Taking the complement of f produces an expression for f in product-of-sums form.
- If Boolean expression is given in form of maxterms (π), then K-map is simplified by following the same procedure as used for SOP form. In this case group of 0's are formed rather than group of 1's.

Q. Simplify $F(A,B,C,D) = \sum(0,1,2,5,8,9,10)$ using K-map.

Sol: 4 variable Boolean expression will have 16 terms ($2^4=16$)

		CD			
		00	01	11	10
AB	00	0	1	3	2
	01	4	5	7	6
	11	12	13	15	14
	10	8	9	11	10

		AB			
		00	01	11	10
CD	00	0	4	12	8
	01	1	5	13	9
	11	3	7	15	11
	10	2	6	14	10

A	B	C	D	Decimal Value
0	0	0	0	0
0	0	0	1	1
0	0	1	0	2
0	0	1	1	3
0	1	0	0	4
0	1	0	1	5
0	1	1	0	6
0	1	1	1	7
1	0	0	0	8
1	0	0	1	9
1	0	1	0	10
1	0	1	1	11
1	1	0	0	12
1	1	0	1	13
1	1	1	0	14
1	1	1	1	15

$$\begin{aligned}
 (1001)_2 &= (9)_{10} \\
 &= 1 \times 2^0 + 0 \times 2^1 + 0 \times 2^2 + 1 \times 2^3 \\
 &= 1 + 8 \\
 &= 9 \\
 (1011)_2 &= (11)_{10} \\
 &= 1 \times 2^0 + 1 \times 2^1 + 0 \times 2^2 + 1 \times 2^3 \\
 &= 1 + 2 + 8 \\
 &= 11
 \end{aligned}$$

$$F = \sum(0, 1, 2, 5, 8, 9, 10)$$

AB \ CD	00	01	11	10
00	1	1		1
01		1		
11				
10	1	1		1

⇒ Group: 1

A	B	C	D
0	0	0	0
0	0	1	0
1	0	0	0
1	0	1	0

$B' D'$

AB \ CD	00	01	11	10
00	1	1		1
01		1		
11				
10	1	1		1

∴ Each group should be as large as possible and groups may overlap

⇒ Group: 2

A	B	C	D
0	0	0	0
0	0	0	1
1	0	0	0
1	0	0	1

$B' C'$

AB \ CD	00	01	11	10
00	1	1		1
01		1		
11				
10	1	1		1

⇒ Group: 3

A	B	C	D
0	0	0	1
0	1	0	1

$A' C' D$

∴ Simplified Function in sum-of-products form:
 $F = B' D' + B' C' + A' C' D$

$$F(A, B, C, D) = \Sigma(0, 1, 2, 5, 8, 9, 10)$$

	AB			
CD	00	01	11	10
00	1	1	1	1
01	1	1	1	1
11				
10	1			1

⇒ Group: 1

A	B	C	D
0	0	0	0
1	0	0	0
0	0	1	0
1	0	1	0

$B' D'$

	AB			
CD	00	01	11	10
00	1			1
01	1	1		1
11				
10	1			1

⇒ Group: 2

A	B	C	D
0	0	0	0
1	0	0	0
0	0	0	1
1	0	0	1

$B' C'$

	AB			
CD	00	01	11	10
00	1			1
01	1	1		1
11				
10	1			1

⇒ Group: 3

A	B	C	D
0	0	0	1
0	1	0	1

$A' C' D$

∴ Simplified function in sum-of-products form:
 $F = B' D' + B' C' + A' C' D$

Q: Simplify the Boolean Function $F(A, B, C, D) = \Sigma(0, 1, 2, 5, 8, 9, 11)$ in Product of sums form.

Sol:

AB \ CD	00	01	11	10
00	1	0	0	1
01	1	1	0	1
11	0	0	0	0
10	1	0	0	1

 \Rightarrow Group: 1

A	B	C	D
0	1	0	0
1	1	0	0
0	1	1	0
1	1	1	0

$B D'$

AB \ CD	00	01	11	10
00	1	0	0	1
01	1	1	0	1
11	0	0	0	0
10	1	0	0	1

 \Rightarrow Group: 2

A	B	C	D
0	0	1	1
0	1	1	1
1	1	1	1
1	0	1	1

$C D'$

AB \ CD	00	01	11	10
00	1	0	0	1
01	1	1	0	1
11	0	0	0	0
10	1	0	0	1

 \Rightarrow Group: 3

A	B	C	D
1	1	0	0
1	1	0	1
1	1	1	1
1	1	1	0

$A B$

$\therefore F' = AB + CD + BD'$

Now, take its complement to get the expression in POS form:

$\therefore F = (A' + B') \cdot (C' + D') \cdot (B' + D)$

Ans

Q: Simplify $y(x_1, x_2, x_3, x_4) = \prod(4, 6, 10, 12, 13, 15)$

Sol:

	x_1, x_2			
x_3, x_4	00	01	11	10
00	0	4	12	8
01	1	5	13	9
11	3	7	15	11
10	2	6	14	10

Group: 1

Group: 1

x_1, x_2	x_3, x_4
0 1	0 0
0 1	1 0

$$x_1 + x_2' + x_4$$

Group: 2

x_1, x_2	x_3, x_4
1 1	0 0
1 1	0 1

$$x_1' + x_2' + x_3$$

Group: 3

x_1, x_2	x_3, x_4
1 1	0 1
1 1	1 1

$$x_1' + x_2' + x_4'$$

Group: 4

1 0 1 0

$$x_1' + x_2 + x_3' + x_4$$

⇒ solution is:

$$(x_1 + x_2' + x_4) \cdot (x_1' + x_2' + x_3) \cdot (x_1' + x_2' + x_4') \cdot (x_1' + x_2 + x_3' + x_4)$$

- **Don't Care Condition:**

- Binary Coded Decimal (BCD):
 - BCD is another process for converting decimal numbers into their binary equivalents.
 - It is a form of binary encoding where each digit in a decimal number is represented in the form of bits.
 - These are generally used in digital displays.

DECIMAL NUMBER	BCD
0	0000
1	0001
2	0010
3	0011
4	0100
5	0101
6	0110
7	0111
8	1000
9	1001

Example:

Convert (123)₁₀ in BCD:

1 → 0001

2 → 0010

3 → 0011

So, in BCD is: 000100100011

- **Don't Care Condition:**

- The “Don't care” conditions allow us to replace the empty cell of a K-map to form a grouping of variables.
- While forming groups of cells, we can consider a “Don't care” cell as either 1 or 0 or we can simply ignore the cell.
- Thus, “Don't care” condition can help us to form a larger group of cells.
- It is not always necessary to fill in the complete truth table for some real-world problems. We may have a choice to not fill in the complete table e.g. while dealing with BCD numbers encoded as four bits, we may not care about any codes above the BCD range of (0,1,2,...9). We would not normally care to fill in those codes because those codes (1010,1011,1100,1101,1110,1111) will never exist as long as we are dealing only with BCD encoded numbers. These six invalid codes are don't cares as far as we are concerned. i.e.
 - We do not care what output our logic circuit produces for these don't cares.
- We only use don't care conditions in a group if it simplifies the logic.
- The cross (x) symbol is used to represent the “don't care” cell in K-map.

- **Significance of “Don't care” conditions:**

- Don't care conditions have the following significance with respect to the digital circuit design:
 - **Simplification:** these are used to further simplify the Boolean output expression.
 - **Lesser number of gates:** → make the digital circuit design more economical.
 - **Prevention of Hazards:** Don't care also prevents hazards in digital systems.

- **Example:**

Q. Consider the following Boolean Function together with the don't care terms:

$$F(A, B, C) = \Sigma(0, 2, 6)$$

$$d(A, B, C) = \Sigma(1, 3, 5)$$

Sol: The 3-variable K-map is:

		AB	00	01	11	10
C	0		0	2	6	4
	1		1	3	7	5

\therefore We will put 1 for 0th, 2nd & 6th term & 'X' for 1st, 3rd & 5th term

		AB	00	01	11	10
C	0		1	1	1	4
	1		X	X	7	X

Group: 1 (circled around cells 0, 1, 2, 3)
Group: 2 (circled around cells 0, 2, 4, 6)

Group: 1

A	B	C
0	0	0
0	1	0
0	0	1
0	1	1

A'

Group: 2

A	B	C
0	1	0
1	1	0

BC'

\therefore Simplified expression is $A' + BC'$
 \Rightarrow to implement this expression we will need one AND and one OR gate.

Q. Simplify:

$$F(A, B, C) = \sum(0, 2, 6)$$

AB \ C	00	01	11	10
0	1	1	1	0
1	0	0	0	0

Group 1 is indicated by a bracket under the first row (minterms 0, 1, 2, 3).
Group 2 is indicated by a bracket under the first three columns (minterms 0, 1, 2, 3, 4, 5, 6, 7).

Group 1:

A	B	C
0	0	0
0	1	0

$$A'C'$$

Group 2:

0	1	0
1	1	0

$$BC'$$

\therefore Simplified expression is: $A'C' + BC'$
 \Rightarrow we will need two AND gates and an OR gate.